

Phototriac chip OPTOTRIAC 130

Description

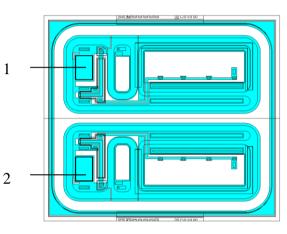
Zero voltage crossing-phototriac chip OPTOTRIAC 130 is designed to be used as phototriac receiver to drive power triacs in phototriacs and to switch AC-circuits in optoelectronic relays` circuits of consumer-oriented industrial automation.

Features

- Chip size 1.5 x 1.3 mm
- Chip thickness 0.36 ± 0.02 mm
- Contact pads size: Terminal 1, 2 - 0.108 mm x 0.152 mm
- Metallization: top AlSi, bottom - Si

Absolute maximum ratings

Storage Temperature	-65°C to 150°C	
Operating Junction Temperature	-55°C to 125°C	
Output Terminal Voltage	600 V	



1 – Terminal 1

2 – Terminal 2

Electrical characteristics (T = 25 °C)

Parameter	Symbol	Min	Тур	Max	Units	Condition
Peak On-State Voltage	V _{TM}	-	1.6	2.0	V	I _{TM} =±100 mA Note 1
Leakage Current in Inhibit State	I _{IH}	-	-	400	μΑ	V _{TM} =±30 V Note 1
Peak Off-State Current	I _{DRM}	-	-	1.0	μΑ	V _{DRM} =±600V Note 2
Inhibit Voltage	V _{IH}	-	-	30	V	Note 1
Critical Rate of Rise Off-State Voltage	dv/dt	-	-	500	V/µs	Note 3

Notes:

1 – Light source with peak wavelength $\lambda = 890 \pm 50$ nm that provides surface irradiance $E_e = 20$ mW/cm² is used.

2 – No light.

3 – Measured in the packaged device.